

HD74LS151

1-of-8 Data Selector / Multiplexer (with strobe)

REJ03D0497-0300 Rev.3.00 May 10, 2006

This data selector / multiplexer contains full-on chip binary decoding to select the desired data source. The HD74LS151 selects one-of-eight data sources and has a strove input, which must be at a low logic level to enable this device. A high level at the strove forces the W output high, and the Y output low.

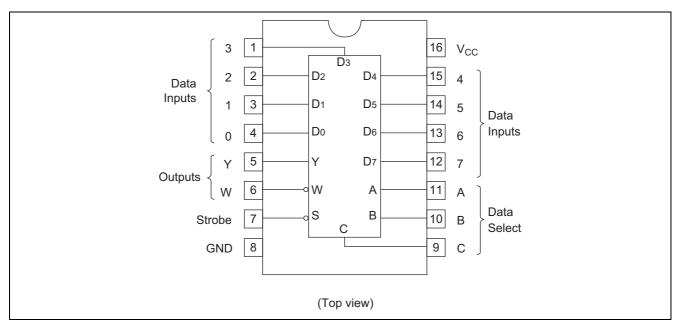
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS151P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74LS151RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

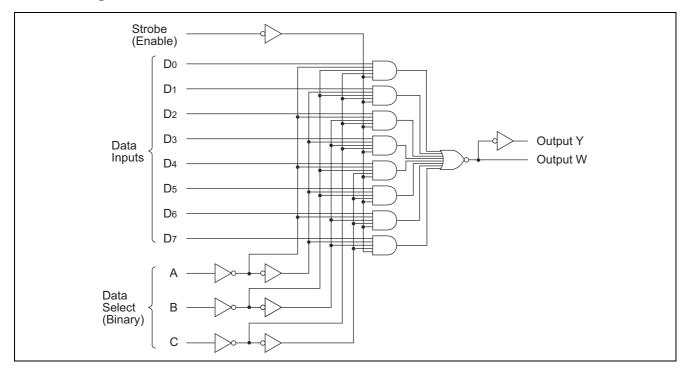


Function Table

	Inp	Outputs			
	Select			>	w
С	В	Α	S	ľ	VV
Х	Х	Х	Н	L	Н
L	L	L	L	D_0	\overline{D}_0
L	L	Н	L	D_1	\overline{D}_1
L	Н	L	L	D_2	\overline{D}_2
L	Н	Н	L	D_3	\overline{D}_3
Н	L	L	L	D_4	\overline{D}_4
Н	L	Н	L	D ₅	\overline{D}_{5}
Н	Н	Ĺ	L	D ₆	\overline{D}_{6}
Н	Н	Н	L	D ₇	\overline{D}_7

H; high level, L; low level, X; irrelevant

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V_{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
Input voltage	V _{IL}	_	_	0.8	V	
Output voltage	V _{OH}	2.7			V	$V_{CC} = 4.75 \; V, \; V_{IH} = 2 \; V, \; V_{IL} = 0.8 \; V, \\ I_{OH} = -400 \; \mu A$
Output voltage	V _{OL}		_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
	VOL	_	_	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$
	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 2.7 \text{ V}$
Input current	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	II	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$
Short-circuit output current	Ios	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current**	Icc		6.0	10.0	mA	V _{CC} = 5.25 V
Input clamp voltage	V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $*V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$

Switching Characteristics

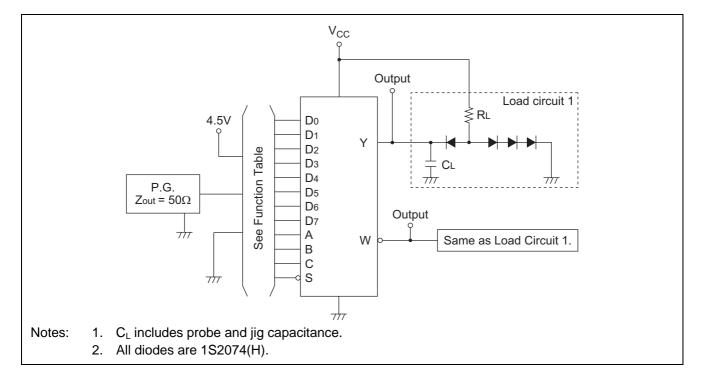
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
	t _{PLH}	A, B, C	Υ		27	43	ns	$C_L = 15 \text{ pF},$ $R_L = 2 \text{ k}\Omega$
	t_{PHL}	(4 Level)	·		18	30		
	t _{PLH}	A, B, C	W		14	23		
	t_{PHL}	(3 Level)	VV		20	32		
	t _{PLH}	Strobe	Y W		26	42		
Propagation delay time	t_{PHL}				20	32		
Fropagation delay time	t _{PLH}	Strobe		_	15	24		
	t_{PHL}				18	30		
	t _{PLH}	D	Y -	_	20	32		
	t_{PHL}				16	26		
	t _{PLH}	D			13	21		
	t _{PHL}	U			12	20		

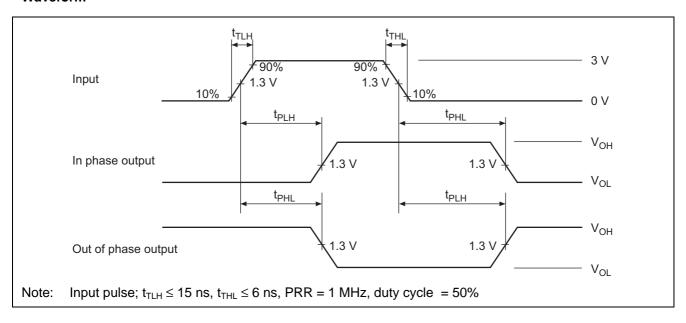
^{**} I_{CC} is measured with all outputs open and all inputs at 4.5 V.

Testing Method

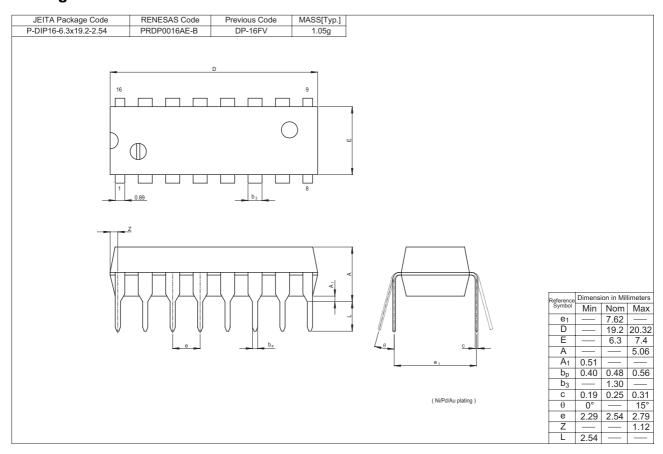
Test Circuit

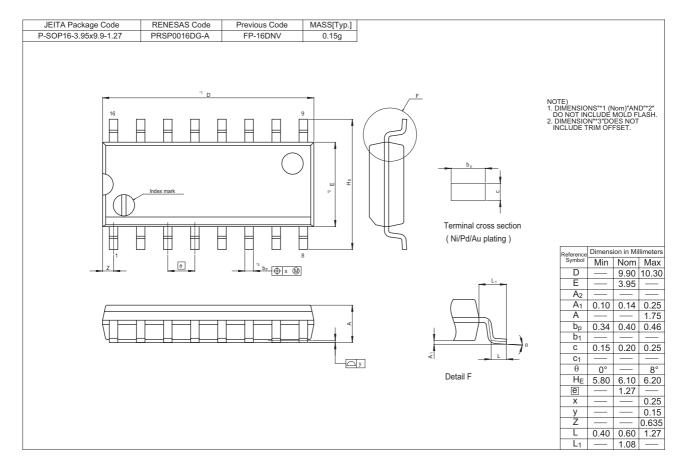


Waveform



Package Dimensions





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